



Institute for form 1449A/PTO			Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)			Application Number	09/945,266
			Filing Date	August 31, 2001
			First Named Inventor	Richard Coulson
			Art Unit	2186
			Examiner Name	W. Choi
Sheet	1	of	Attorney Docket Number	42390P11446

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.†	Document Number Number - Kind Code* (if known)	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document *	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
W/C		US-4430712	02-07-1984	COULSON et al	
		US-4468730	08-28-1984	DODD et al	
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Examiner Initials*	Cite No.†	Foreign Patent Document Country Code* - Number* - Kind Code* (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
W/C		EP 0702305 A1	03-20-1996	KITAGAWA	
		JP 06236241	08-23-1994	SHARP CORP	

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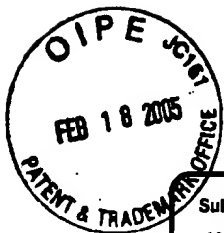
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NON PATENT LITERATURE DOCUMENTS

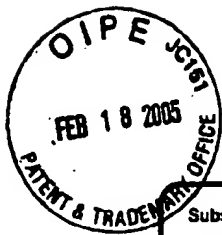
Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	7
Whe		COULSON - US PATENT APPLICATION 09/602,008 "IN-LINE CACHE"	
		COULSON - US PATENT APPLICATION 09/602,009 "NON-VOLATILE CACHE EXPANSION BOARD"	
		COULSON - US PATENT APPLICATION 09/602,010 "NON-VOLATILE CACHE"	
		COULSON - US PATENT APPLICATION 09/602,011 "NON-VOLATILE CACHE INTEGRATED WITH MASS STORAGE DEVICE"	
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		AUCIELLO et al - THE PHYSICS OF FERROELECTRIC MEMORIES, Physics Today, July 1998, pp 22-27	
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		HODGES et al - 1983, pp 388-389 - Analysis and Design of Digital Integrated Circuits	
		INTEL - June 1997 - Intel 430HX PCIsset Design Guide pp 1-1 thru 6-15	

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WLC		INTEL - April 1997 - 82371FB (PIIX) AND 82371SB (PIIX3) PCI ISA IDE XCELERATOR	
		IRE TRANSACTIONS - COMPONENT PARTS - Switching the Memory Matrix	
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